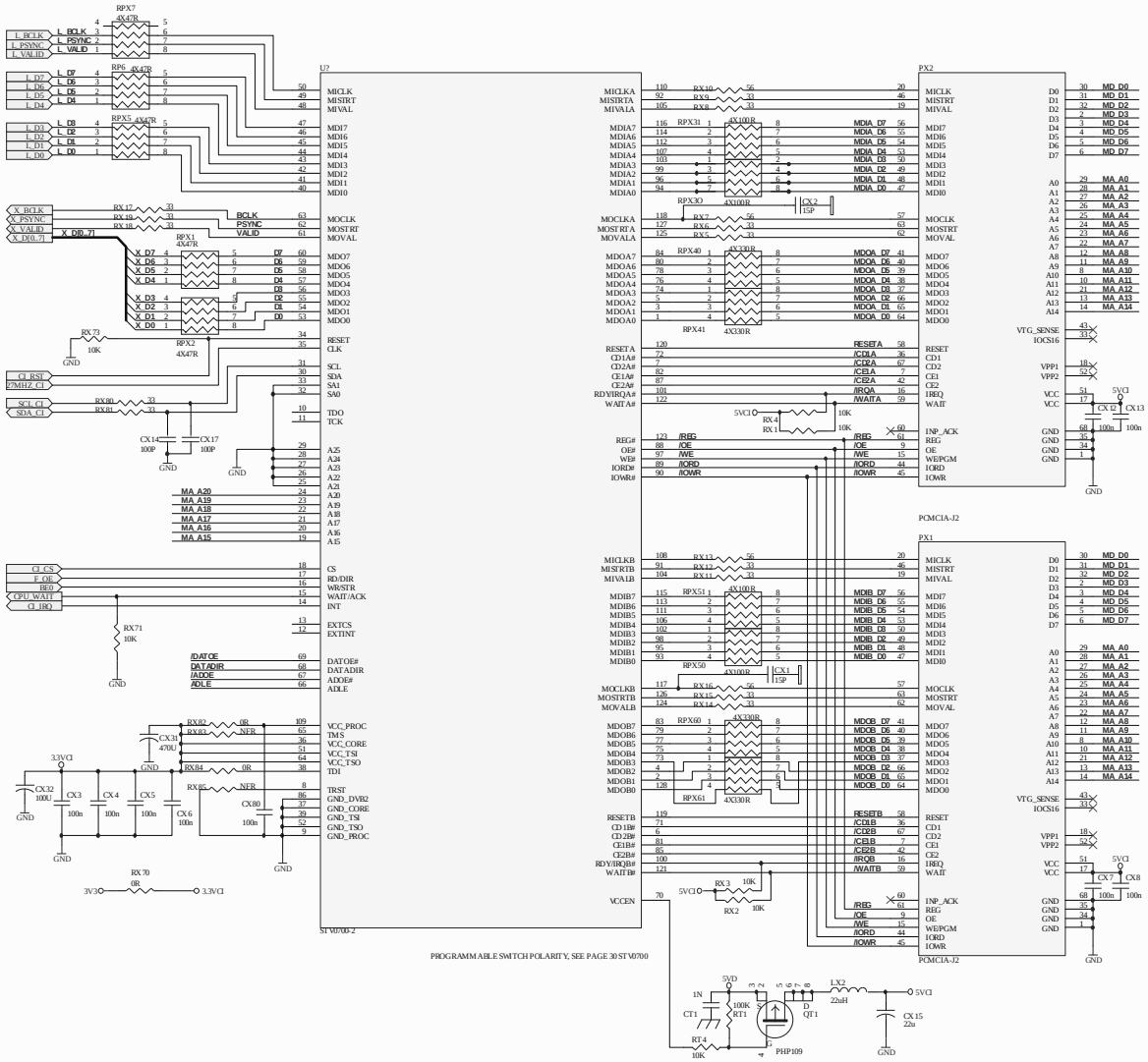
U2
ST V0700-2
PX2
PCMCIA-J2
PX1
PCMCIA-J2
PROGRAMMABLE SWITCH POLARITY, SEE PAGE 30 ST V0700
RPX7 4X47R
RP6 4X47R
RPX5 4X47R
RPX1 4X47R
RPX2 4X47R
RPX30 4X100R
RPX31 4X100R
RPX40 4X330R
RPX41 4X330R
RPX50 4X100R
RPX51 4X100R
RPX60 4X330R
RPX61 4X330R
CX1 15P
CX2 15P
L_BCLK
L_PSYNC
L_VALID
L_D7
L_D6
L_D5
L_D4
L_D3
L_D2
L_D1
L_D0
X_BCLK
X_PSYNC
X_VALID
X_D0..7
CI_RST
27MHZ_CI
SCL_CI
SDA_CI
CI_CS
F_OE
BE0
CPU_WAIT
CI_IRQ
IDATOE
DAT.ADIR
IADOE
ADLE
3.3VCI
5VCI
3V3
GND
PHP109
LX2 22uH
CX15 22u
RX70 0R
RX71 10K
RX73 10K
CX14 100P
CX17 100P
CX32 100U
CX3 100n
CX4 100n
CX5 100n
CX6 100n
CX80 100n
CX31 470U
RX82 0R
RX83 NFR
RX84 0R
RX85 NFR
RX80 33
RX81 33
RX4 10K
RX1 10K
RX3 10K
RX2 10K
RT4 10K
CT1 1N
RT1 100K
QT1
CX7 100n
CX8 100n
CX12 100n
CX13 100n
RPX20 4X33R
RPX21 4X33R
RPX70 4X33R
RPX71 4X33R
RPX80 4X33R
RPX81 4X33R
RPX3 4X47R
RPX4 4X47R
RX86 10K
IC4 74HC245
IC3 74HC373
IC2 74HC373
CX9 100n
CX10 100n
CX11 100n
CX33 100U
DATA[0..15]
DATA[0..15]
ADDR[1..21]
ADDR[1..21]
BE1
Cannot open file E:_CAD/CIRC/UITSSK_UK.BMP
Drg.No. CI6.SCH
Description CI SECTION
Date 9-10-02
Issue 2.0
Model DG30