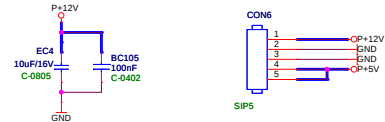
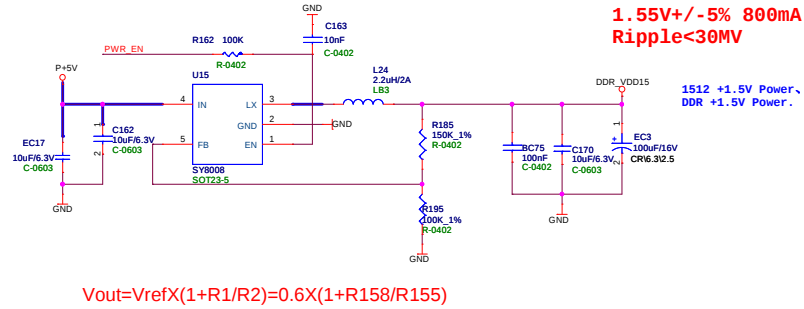


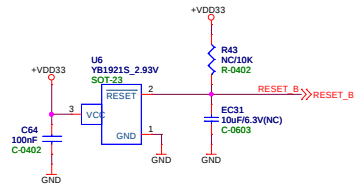
System Power Input Connector



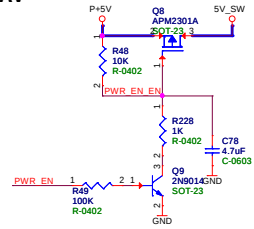
1.5V for DDRIII & SPHE1512 DDRIII Circuit



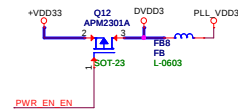
Reset Circuit



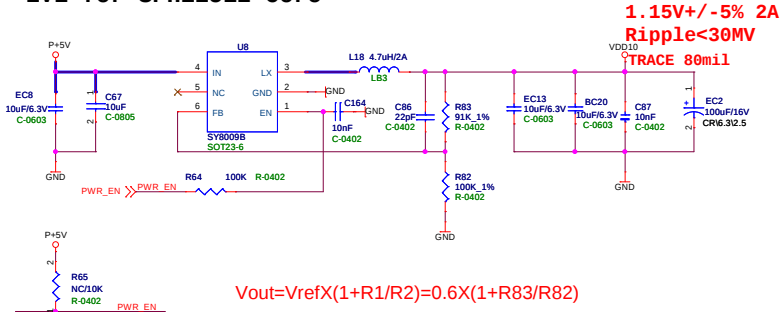
5V Switching for Tuner & USB & AV



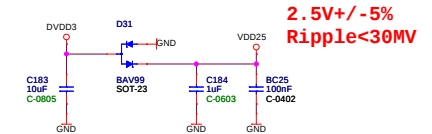
3.3V for SYSTEM



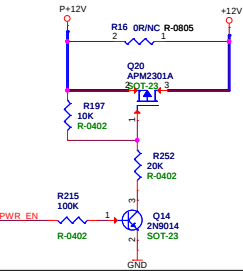
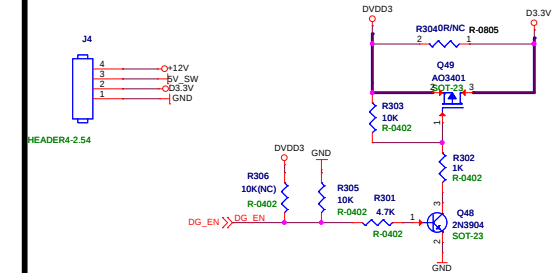
1V1 for SPHE1512 Core



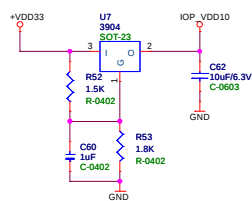
2.5V for MPLL&OTP



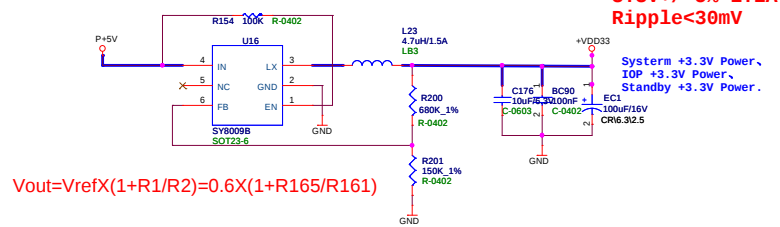
DONGLE Power Input Connector



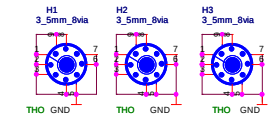
Stand-by power supply for IOP



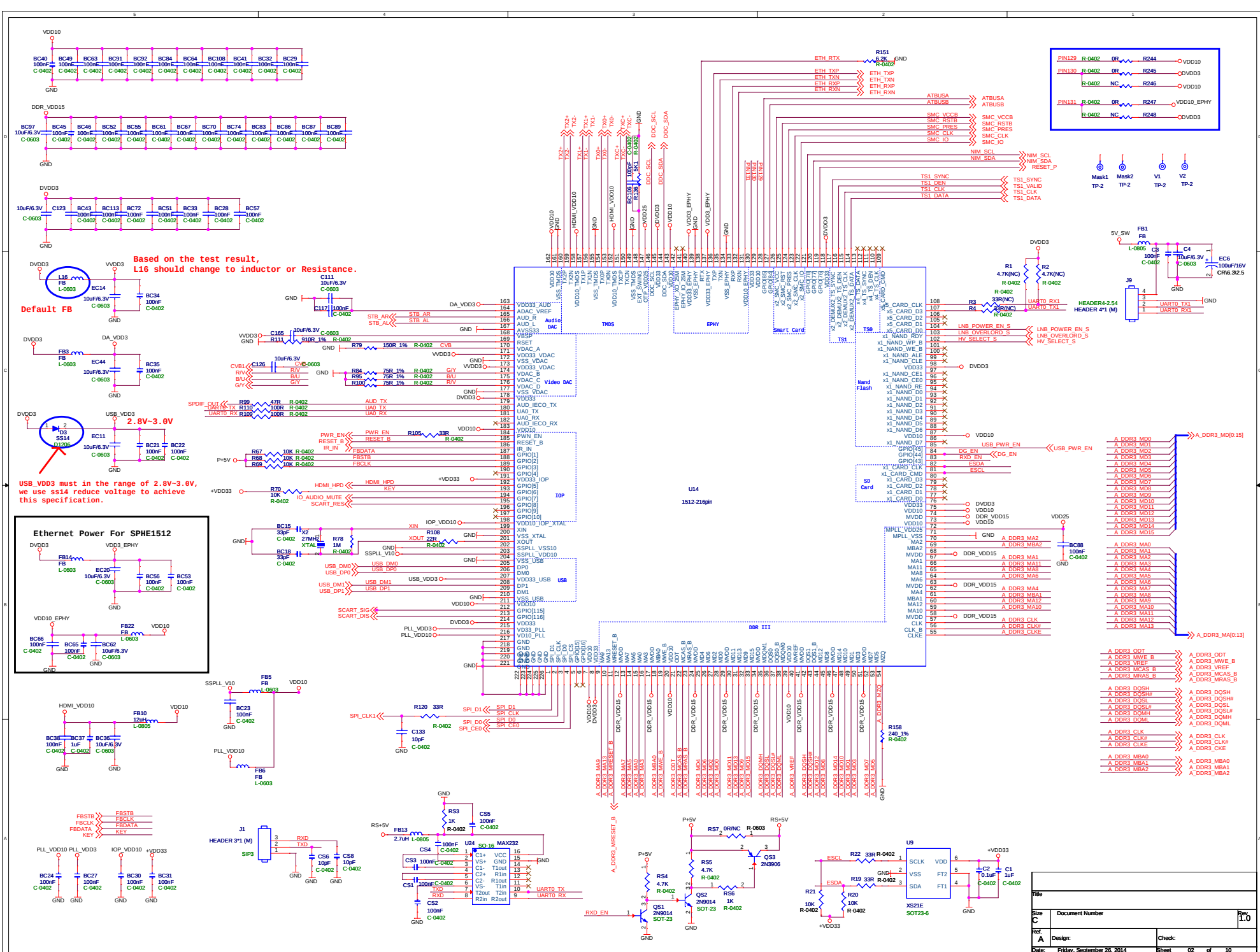
3.3V for System & IOP Circuit



SCREW HOLE



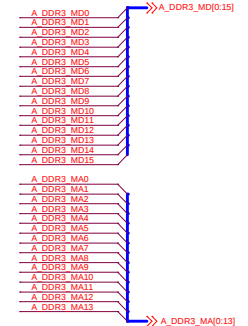
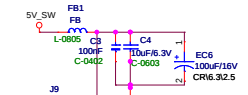
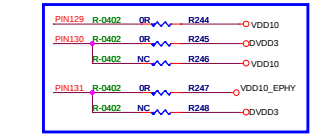
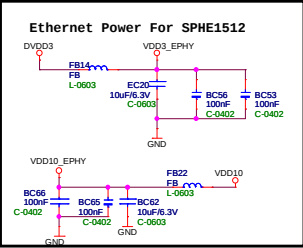
Title		
Size	Document Number	Rev
Custom	Power	1.0
Ref.	Design:	Check:
A	Date: Friday, September 26, 2014	Sheet 01 of 10



Based on the test result,
L16 should change to inductor or Resistance.

Default FB

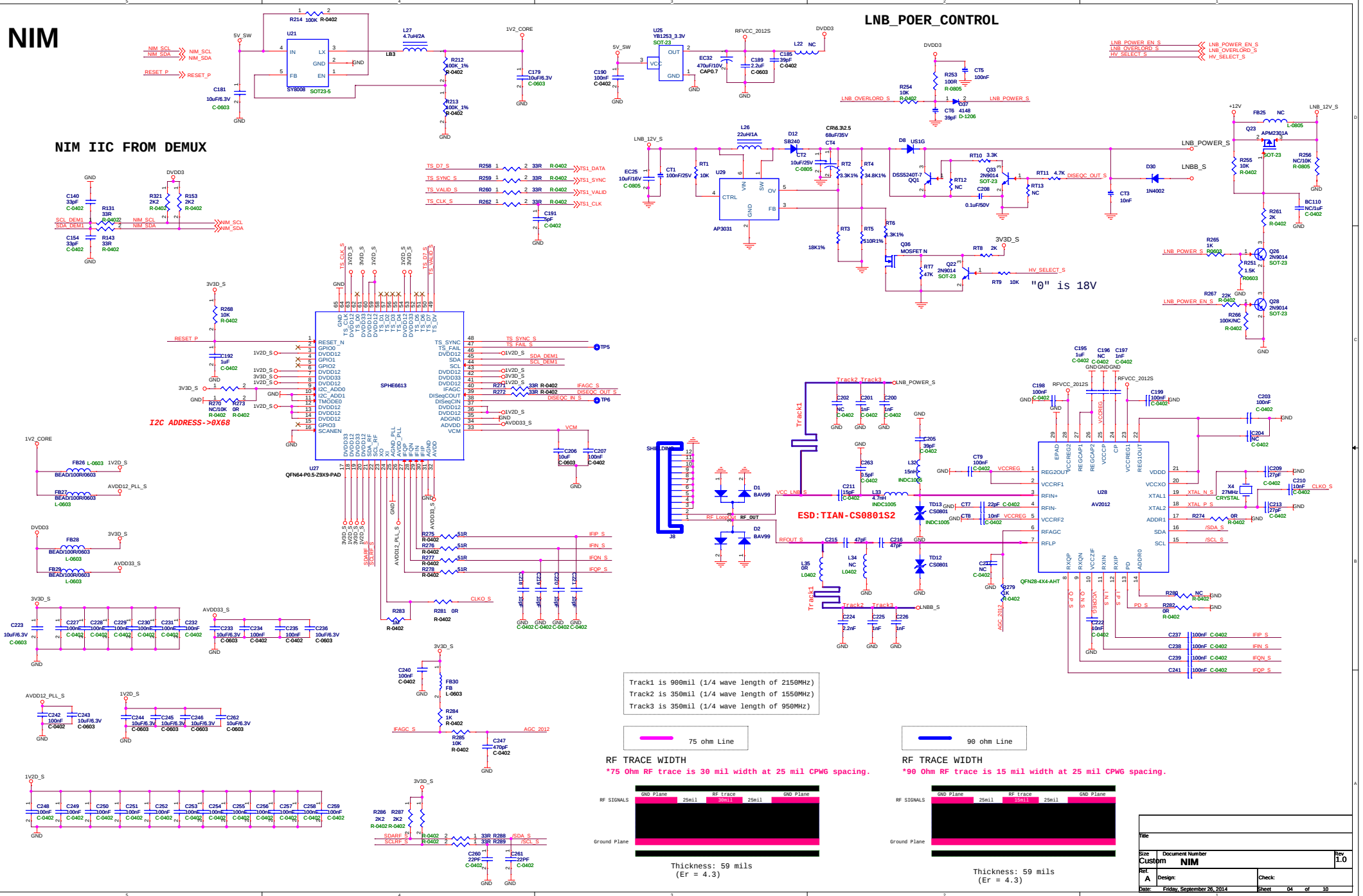
USB_VDD3 must in the range of 2.8V-3.0V,
we use ss14 reduce voltage to achieve
this specification.



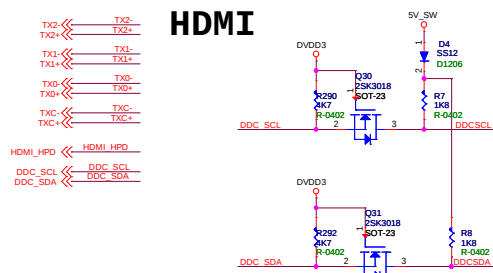
Title		Rev. 1.0
Size	Document Number	
File	Design:	Check:
Date: Friday, September 26, 2014	Sheet	02 of 10

NIM

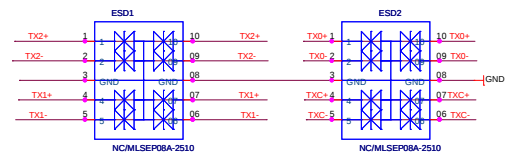
NIM IIC FROM DEMUX

LNB_POER_CONTROL

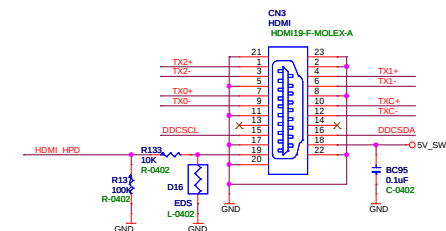
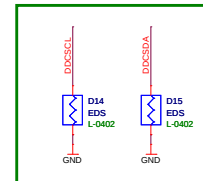
HDMI



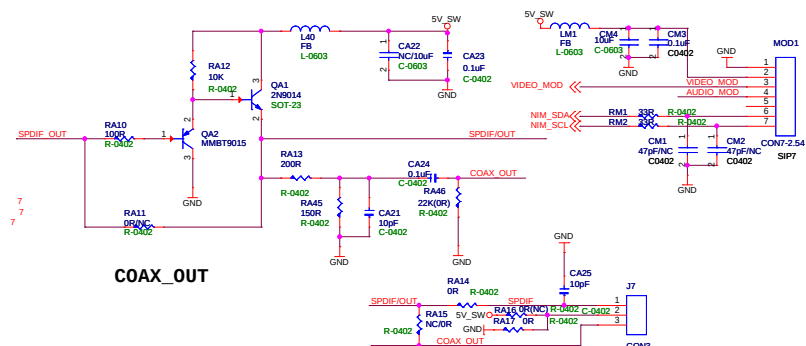
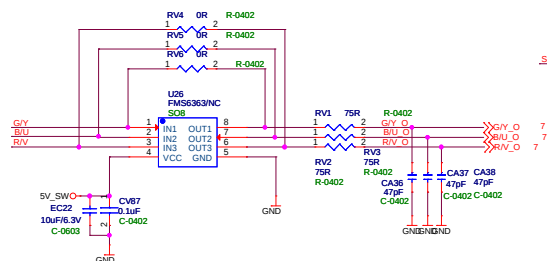
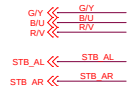
DATA Line ESD



Commend Line ESD



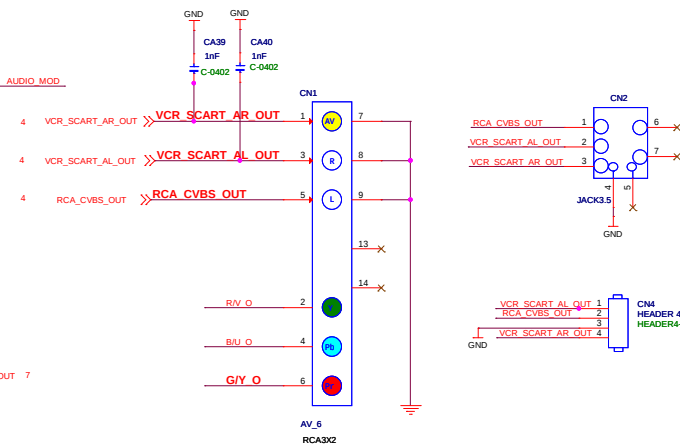
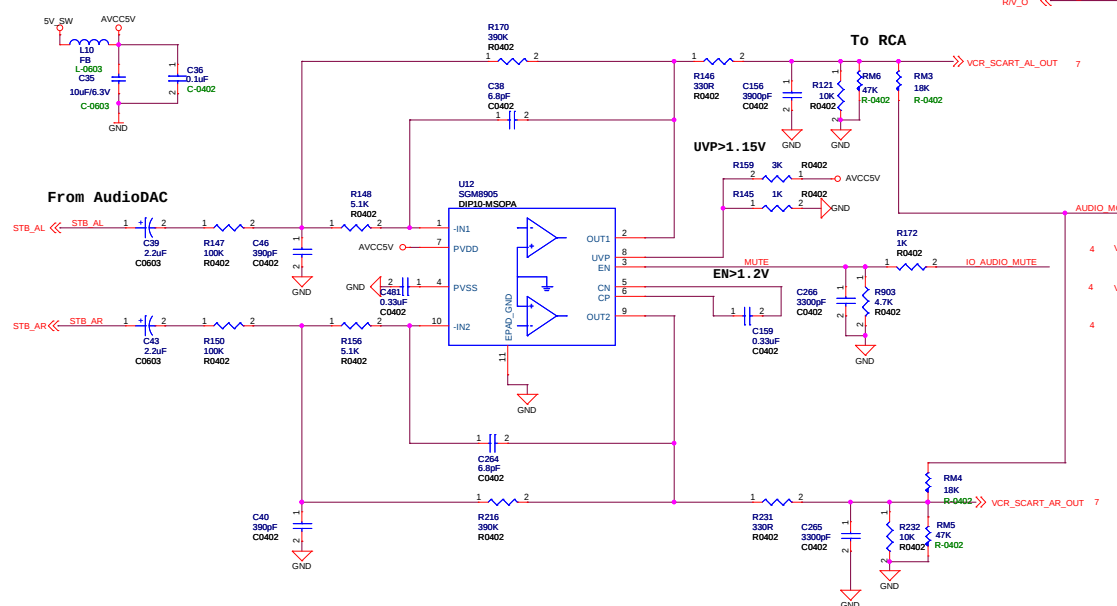
CVBS & RGB & YPbPr



COAX_OUT



J7接SPDIF时, RA46 (22K--->0R), CA24(0.1uF--->NC), RA17(0R--->NC), RA16(NC--->0R), RA14(NC--->0R)
J7接Coaxial Audio时, RA46 (0R--->22K), CA24(NC--->0.1uF), RA15(NC--->0R), RA17(NC--->0R), RA16(0R--->NC), RA14(0R--->NC)



Title				
Size C	Document Number AV OUT			Rev. 1.0
Ref. A	Design:		Check:	
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TV SCART

*near to
Scart
connector*

Title			
Size B	Document Number SCART		Rev 1.0
Ref. A	Design:	Check:	
Date:	Friday, September 26, 2014		Sheet 07 of 10

5	4
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Figure 6



2